



NEW HORIZON COLLEGE OF ENGINEERING

New Horizon Knowledge Park, Ring Road, Marathahalli
Autonomous College Permanently Affiliated to VTU, Approved by AICTE & UGC
Accredited by NAAC with 'A' Grade, Accredited by NBA

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

BOARD OF STUDIES MEETING

DATE : 28.05.2022
VENUE : COE-Schneider Electric, EEE Department
TIME : 09.30 am - 12.00pm

Head of the Department
Department of Electrical and Electronics Engineering
New Horizon College of Engineering
Ring Road, Madubisanahalli, Bellandur Post
Bangalore - 550103, Karnataka, India

CONTENTS

Sl. No	PARTICULARS	Page No
1	Agenda for the meeting	3
2	List of Members	4
3	List of Members present	6
4	Welcome address by Chairman of BOS and Introduction of members	8
	Agenda 1: Implementation of revised syllabus details based on previous BoS meeting	8
5	Agenda 2: Approval of Scheme & Syllabus of II Year 2021-2024 Batch (III & IV semesters) as per NEP	10
6	Agenda 3: Approval of Scheme & Syllabus of III year and IV year of 175 scheme	13
7	Agenda 4: Discussion on rubrics of Project	19
8	Recommendations of the Board	21
9	Vote of thanks by Chairman of BoS	22

AGENDA FOR THE MEETING

Agenda 1: Implementation of revised syllabus details based on previous BoS meeting

Agenda 2: Approval of Scheme & Syllabus of II Year 2021-2025 Batch (III & IV semesters) as per NEP

Agenda 3: Approval of Scheme & Syllabus of III year and IV year of 175 scheme

Agenda 4: Discussion on rubrics of Project

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

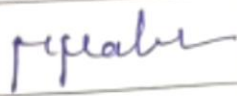








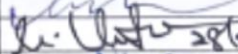

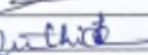

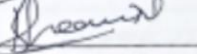
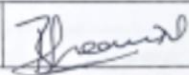
LIST OF BOS MEMBERS FOR THE AY: 2022-2023

S. No	Category	Nomination of the committee	Name of the person	Designation & Affiliation
1	Head of the Department	Chairperson	Dr. Mahesh M	HoD/Professor, NHCE, Bangalore
2	Special Invitees (one academician from Institution of National Eminence, IIT,NIT,IIM,IISC)	1	Dr. Manjunatha	Principal, NHCE
		2	Dr. R. J Anandhi	Dean Academics, NHCE
		3	Dr. L Umanand	Professor, Center for Electronics and Design Technology (CEDT), Indian Institute of Science, Bangalore, lums@iisc.ac.in
		4	Dr. Sanjeev Sharma	Professor & Dean - Quality Assurance and Skill Development Center, NHCE
3	Faculty member at different level with different specialization	Members		
		1	Dr.Sujitha S	Associate Professor, NHCE
		2	Dr.Gunapriya B	Associate Professor, NHCE
		3	Dr.Vinoth Kumar K	Associate Professor, NHCE
		4	Dr. Joshua Daniel Raj J	Senior Assistant Professor, NHCE
		5	Mr.Inbasakaran S	Senior Assistant Professor, NHCE
		6	Ms.Karthika M	Senior Assistant Professor, NHCE
4	Subject expert from outside the college nominated by Academic	Members		
		1.	Dr..K.Shanmukha Sundar	Professor, Dayananda Sagar Academy of Technology and Management, Bengaluru -560082 shanmukhasundar-eee@dsatm.edu.in

	Council			
5	Experts from outside the college nominated by VTU	Member		
		1	Dr. .Lakshminarayana C	Professor, BMS college of Engineering, Bengaluru-560019
6	Representative from Industry / Corporate sector / allied area related to placements, nominated by Academic Council	Members		
		1	Dr. V Kamalakannan	Senior Design Engineer Tessolve Semiconductor Pvt. Ltd., Plot No: 31 (P2), Electronic City Phase II, Bangalore – 560 100, Karnataka, India. kamalakannanvs@hotmail.com
		2	Mr. Rajashekhar S	General Manager, Open Systems International (An Emerson Company), Bengaluru, Karnataka, India. rajashekhar.sammeta@osii.com
7	Meritorious alumni nominated by Principal	Members		
		1	Mr. Bhavan N	Controls system engineer, Quest global engineering Pvt Ltd, bhavannreddy@gmail.com
		2	Mr. Naimish Kumar Bareek,	Trainee Automation Engineer, Aideas Engineering Pvt Ltd, babubareek@gmail.com
8	Co-opted members	Members		
		1	Dr.Singaravelan A	Senior Assistant Professor, NHCE
		2	Mr.Vinodkumar S	Senior Assistant Professor, NHCE
		3	Mr.Muni Prakash T	Senior Assistant Professor, NHCE
		4	Ms. Deepa V B	Senior Assistant Professor, NHCE

		5	Mr. Sunil S K	Senior Assistant Professor, NHCE
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LIST OF BOS MEMBERS

S.NO	NAME	DESIGNATION & AFFILIATION	SIGNATURE
1	Dr. Mahesh M	BoS Chairman HoD/Professor, NHCE, Bangalore	
Special Invitees			
2	Dr. Manjunatha	Principal, NHCE	
3	Dr. R. J Anandhi	Dean Academics, NHCE	
4	Dr. L Umanand	Professor, Center for Electronics and Design Technology (CEDT), Indian Institute of Science, Bangalore, lums@iisc.ac.in	-
5	Dr. Sanjeev Sharma	Professor & Dean - Quality Assurance and Skill Development Center, NHCE	
Academic Expert			
6	Dr..K.Shanmukha Sundar	Professor, Dayananda Sagar Academy of Technology and Management, Bengaluru -560082 shanmukhasundar-eee@dsatm.edu.in	
VTU nominee			
7	Dr. .Lakshminarayana C	Professor, BMS college of Engineering, Bengaluru-560019	
Industry Experts			
8	Mr. Rajashekhar S	General Manager, Open Systems International (An Emerson Company), Bengaluru, Karnataka, India.rajashekhar.sammata@osii.com	
9	Dr. V Kamalakannan	Senior Design Engineer Tessolve Semiconductor Pvt. Ltd., Plot No: 31 (P2), Electronic City Phase II, Bangalore - 560 100, Karnataka, India. kamalakannanvs@hotmail.com	
Faculty member at different level with different specialization			
10	Dr.Sujitha S	Associate Professor, NHCE	
11	Dr.Gunapriya B	Associate Professor, NHCE	
12	Dr.Vinoth Kumar K	Associate Professor, NHCE	
13	Dr. Joshua Daniel Raj J	Senior Assistant Professor, NHCE	
14	Mr.Inbasakaran S	Senior Assistant Professor, NHCE	
15	Ms.Karthika M	Senior Assistant Professor, NHCE	
Meritorious alumni			
16	Mr. Bhavan N	Controls system engineer, Quest global engineering Pvt Ltd,	

		bhavannreddy@gmail.com	
17	Mr. Naimish Kumar Bareek,	Trainee Automation Engineer, Aideas Engineering Pvt Ltd, babubareek@gmail.com	<i>Bareek</i>
Co-opted faculty members			
18	Dr.Singaravelan A	Senior Assistant Professor, NHCE	<i>SA</i>
19	Mr.Vinodkumar S	Senior Assistant Professor, NHCE	-
20	Mr.Muni Prakash T	Senior Assistant Professor, NHCE	<i>T</i>
21	Ms. Deepa V B	Senior Assistant Professor, NHCE	<i>Deepa</i>
22	Mr. Sunil S K	Senior Assistant Professor, NHCE	<i>Sunil</i>

NEW HORIZON COLLEGE OF ENGINEERING

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

WELCOME ADDRESS BY THE CHAIRMAN OF BOS AND INTRODUCTION OF MEMBERS

Minutes

Dr. Mahesh M, Chairman of BOS, welcomed the BoS members and introduced the significance of autonomy in the context of engineering education from industry perspective. The chairman briefed the gathering about the various regulations being followed in the department and emphasized the need for revision in curriculum and syllabi based on the inputs from various stake holders.

AGENDA -1

Implementation of revised syllabus details based on previous BoS meeting

Minutes

- Based on the previous BoS meeting suggestions, the following courses were implemented and incorporated in the curriculum.

Year	Semester	Current Course Code and Course Name (Before BOS)	Proposed Course Code and Course Name (After BOS)	Percentage change of syllabus	Remarks if any
I Year	I & II	19EEE15/25- Basic Electrical Engineering	21EEE15A/25A- Basic Electrical Engineering	20%	➤ As per the industry requirement Electro chemical power sources are included.
I Year	I & II	19EEL17/27- Basic Electrical Engineering Laboratory	21EEL17A/27A- Basic Electrical Engineering Laboratory	-	➤ Course code has been changed for 160 credit scheme
III Year	V	20EEE561- Data structure and Algorithm	21EEE561- object-oriented programming using C++ and JAVA	40 %	➤ The course name is changed as object-oriented programming using C++ and JAVA. ➤ The data structures were implemented using C language in the course 20EEE561 (Data structure and Algorithm) in the previous syllabus. ➤ As the object-oriented programming (OOPs) concept dominates the present programming domain, the python programming is

					<p>introduced to implement the data structure in VI semester.</p> <ul style="list-style-type: none"> ➤ Hence it is proposed to introduce the object-oriented programming concepts in the V semester using C++ and JAVA. ➤ It provides the complete insights about OOPs.
III Year	VI	20EEE641- object-oriented programming	21EEE641- Data structure and Algorithm using Python	100%	<ul style="list-style-type: none"> ➤ The course name is changed as Data structure and Algorithm using Python. ➤ The required OOPs concepts were covered in the course 21EEE561 (object-oriented programming using C++ and JAVA) in V semester. ➤ In order to cope up with present programming trends, the Python is introduced in Data structure and Algorithm. ➤ The python language is introduced to implement stack, queues, linked list, tree and graph data structures in the course 21EEE641 (Data structure and Algorithm using Python).
III Year	VI	20EEE652- VLSI design	20EEE652A- CMOS VLSI design	85%	<ul style="list-style-type: none"> ➤ The reframed CMOS VLSI syllabus focusses on CMOS technology, Design of combinational and sequential circuits and timing analysis of logic circuits ➤ In order to meet the requirements of semiconductor industries the revised syllabus focusses on advances in AI, EV, and smartphone technologies which demands VLSI engineers at IC design companies. ➤ Hence the course name and course content of VLSI design is changed as CMOS VLSI design.

AGENDA -2

Approval of Scheme & Syllabus of II Year 2021-2025 Batch (III & IV semesters) as per NEP Minutes

- *Scheme & Syllabus of II-year -2021-25 Batch (III & IV semesters) has been reviewed.*
- *Suggestions from BoS members have been acknowledged and discussed in detail.*
- *Scheme & Syllabus of II Year 2021-2025 Batch (III & IV semesters) as per NEP has been

unanimously approved by all the members.*

New Horizon College of Engineering
Department of Electrical and Electronics Engineering
Scheme of Third Semester B.E Program

S. No	Course Code	Course	BoS	Credit Distribution				Overall Credits	Contact Hours	Marks		
				L	T	P	S			CIE	SEE	Total
1	21EEE31A	Applied Mathematics-III	AS	2	1	0	0	3	4	50	50	100
2	21EEE322A	Digital system design using verilog	EEE	1	0	1	0	2	3	50	50	100
3	21HSS332A / 21HSS333A	Aadalitha Kannada / Vyavaharikha Kannada	HSS	1	0	0	0	1	1	50	50	100
4	21HSS342A	Environmental Science	HSS	1	0	0	0	1	1	50	50	100
5	21EEE35A	Analog and linear integrated circuits	EEE	2	1	0	0	3	4	50	50	100
6	21EEE36A	Electric circuit theory	EEE	2	1	0	0	3	4	50	50	100
7	21EEE37A	DC machines and transformers	EEE	2	1	0	0	3	4	50	50	100
8	21EEL35A	Analog and linear integrated circuits Laboratory	EEE	0	0	1	0	1	2	50	50	100
9	21EEL36A	Electric circuit theory Laboratory	EEE	0	0	1	0	1	2	50	50	100
10	21EEL37A	DC machines and transformers Laboratory	EEE	0	0	1	0	1	2	50	50	100
11	21EEE38A	Mini Project- I	EEE	0	0	2	0	2	4	50	50	100
12	21DMAT31A	Basic Applied Mathematics-I	AS	0	0	0	0	0	2	50	50	100
Total								21	31/33	550/ 600	550/ 600	1100/ 1200

New Horizon College of Engineering
Department of Electrical and Electronics Engineering
Scheme of Fourth Semester B.E Program

S. No	Course Code	Course	BoS	Credit Distribution				Overall Credits	Contact Hours	Marks		
				L	T	P	S			CIE	SEE	Total
1	21EEE41A	Applied Mathematics-IV	AS	2	1	0	0	3	4	50	50	100
2	21HSS421A	Life Skills for Engineering	HSS	1	0	1	0	2	3	50	50	100
3	21HSS431A	Entrepreneurship Development -II	HSS	1	0	0	0	1	1	50	50	100
4	21HSS441A	Constitution of India & Professional Ethics	HSS	1	0	0	0	1	1	50	50	100
5	21EEE45A	Control systems	EEE	2	1	0	0	3	4	50	50	100
6	21EEE46A	Synchronous and induction machines	EEE	2	1	0	0	3	4	50	50	100
7	21EEE47A	Microcontroller and embedded systems	EEE	2	1	0	0	3	4	50	50	100
8	21EEL45A	Control systems Laboratory	EEE	0	0	1	0	1	2	50	50	100
9	21EEL46A	Synchronous and induction machines Laboratory	EEE	0	0	1	0	1	2	50	50	100

10	21EEL47A	Microcontroller and embedded systems Laboratory	EEE	0	0	1	0	1	2	50	50	100
11	21EEE48A	Summer Internship - I	EEE	0	0	0	2	2	0	50	50	100
12	21DMAT41A*	Basic Applied Mathematics-II	HSS	0	0	0	0	0	2	50	50	100
13	21DAEC40A*	Communicative English	AS	0	0	0	0	0	2	50	50	100
Total								21	27/31	550/650	550/650	1100/1300

*Applicable to Diploma students

AGENDA -3

Approval of Scheme & Syllabus of III year and IV year of 175 scheme

- *Scheme & Syllabus of III year (V and VI semester) and VI year (VII and VIII semester)*

courses were approved in the previous BoS meeting and the same will be continued in the AY: 2022-2023.

Scheme of Fifth Semester B.E Program

Sl. No	Course Code	Course	BOS	Credit Distribution				Overall Credits	Contact Hours per Week	Marks		
				L	T	P	S			CIE	SEE	Total
1	20EEE51	Transmission and Distribution	EEE	3	0	0	0	3	3	50	50	100
2	20EEE52	Control Systems	EEE	3	0	0	0	3	3	50	50	100
3	20EEE53	Synchronous and Induction Machines	EEE	3	0	0	0	3	3	50	50	100
4	20EEE54	Signals and Systems	EEE	3	0	0	0	3	3	50	50	100
5	20EEE55	Industrial Automation	EEE	3	0	0	0	3	3	50	50	100
6	20EEE56X/ 20EEE56XA	Professional Elective I	EEE	3	0	0	0	3	3	50	50	100
7	20EEL57	Control Systems Laboratory	EEE	0	0	1.5	0	1.5	3	25	25	50
8	20EEL58	Synchronous and Induction Machines Laboratory	EEE	0	0	1.5	0	1.5	3	25	25	50
9	20EEL59	Mini Project III	EEE	0	0	2	0	2	4	25	25	50
		TOTAL						23	28	375	375	750

Professional Elective I	
Course Code	Course
20EEE561A	Object Oriented programming using C++ and JAVA
20EEE562	Modern Communication Systems
20EEE563	Advanced Micro Controller and Applications
20EEE564	MEMS and Applications

Scheme of Sixth Semester B.E Program

S. No	Course Code	Course	BOS	Credit Distribution				Overall Credits	Contact Hours per Week	Marks		
				L	T	P	S			CIE	SEE	Total
1	20EEE61	Power System Analysis	EEE	3	0	0	0	3	3	50	50	100
2	20EEE62	Power Electronics	EEE	3	0	0	0	3	3	50	50	100
3	20EEE63	Power System Protection	EEE	3	0	0	0	3	3	50	50	100
4	20EEE64X/2 0EEE64XA	Professional Elective II	EEE	3	0	0	0	3	3	50	50	100
5	20EEE65X/ 20EEE65XA	Professional Elective III	EEE	3	0	0	0	3	3	50	50	100
6	20NHOP6X X	Open Elective-I	EEE	3	0	0	0	3	3	50	50	100
7	20EEL66	Power System Analysis Laboratory	EEE	0	0	1.5	0	1.5	3	25	25	50
8	20EEL67	Power Electronics Laboratory	EEE	0	0	1.5	0	1.5	3	25	25	50
9	20EEL68	Mini Project IV	EEE	0	0	2	0	2	4	25	25	50
TOTAL								23	28	375	375	750

Professional Elective II	
Course Code	Course
20EEE641A	Data structures and algorithms using python
20EEE642	Fiber Optic and Laser Instrumentation
20EEE643	Robotics and Automation
20EEE644	Virtual Instrumentation
Professional Elective III	
Course Code	Course
20EEE651	Operation Research
20EEE652A	CMOS VLSI Design

20EEE653	Advanced Industrial and Building Automation
20EEE654	Advanced Control Systems

Scheme of seventh semester

Sl. No	Course Code	Course	Credit Distribution				Overall Credits	Contact Hours per Week	Marks			
			L	T	P	S			CIE	SEE	Total	
1	20EEE71A	Special Electrical Machines	3	0	0	0	3	3	50	50	100	
2	20EEE72A	Relay and High voltage Engineering	3	0	0	0	3	3	50	50	100	
3	20EEE73A	Electrical Drives and vehicles	3	0	0	0	3	3	50	50	100	
4	20EEE74XA	Professional Elective IV	3	0	0	0	3	3	50	50	100	
5	20EEE75XA	Professional Elective V	3	0	0	0	3	3	50	50	100	
6	20NHOP7XX	Open Elective- II	3	0	0	0	3	3	50	50	100	
7	20EEL76A	Relay and High voltage Engineering laboratory	0	0	1.5	0	1.5	3	25	25	50	
8	20EEL77A	Simulation tools for Electrical Engineering laboratory	0	0	1.5	0	1.5	3	25	25	50	
9	20EEE78A	Project Phase I	0	0	2	0	2	4	50	50	100	
TOTAL								23	28	400	400	800

Professional Elective IV	
Course Code	Course
20EEE741A	Digital Signal Processing
20EEE742A	FACTS and HVDC transmission
20EEE743A	Testing and Commissioning
20EEE744A	Energy Auditing and Demand side Management
Professional Elective V	
Course Code	Course
20EEE751A	Utilization of Electrical Energy
20EEE752A	Power System Operation and Control
20EEE753A	Professional Ethics
20EEE754A	Neural network and Fuzzy logic in Electrical Engineering

Scheme of Eighth Semester B.E Program

Sl. No	Course Code	Course	Credit Distribution				Overall Credits	Contact Hours per Week	Marks		
			L	T	P	S			CIE	SEE	Total
1	20EEE81XA	Professional Elective VI	3	0	0	0	3	3	50	50	100
2	20EEE82XA	Professional Elective VII	3	0	0	0	3	3	50	50	100
3	20EEE83A	Internship	0	0	4	0	4	8	50	50	100
4	20EEE84A	Project Phase II	0	0	10	0	10	20	50	50	100
TOTAL							20	34	200	200	400

Professional Elective VI		
Sl No	Course Code	Course
1	20EEE811A	Estimation and Costing of electrical systems
2	20EEE812A	Smart Grid Technologies
3	20EEE813A	Power Quality
4	20EEE814A	Integration of distributed generation

Professional Elective VII		
Sl No	Course Code	Course
1	20EEE821A	Photo Voltaic Systems and Applications
2	20EEE822A	Simulation of Power Electronics
3	20EEE823A	Biomedical Instrumentation
4	20EEE824A	Applications of IOT in Electrical Engineering

AGENDA -4

Discussion on rubrics of Project

The following rubrics has been followed for the Project based learning.

Table 1: Rubrics for project Based Learning

Review	Agenda	Description	Assessment	Mapped PO	PSOs	Marks
First review	Project scopes and Proposal	Identification of Problem Domain and detailed Analysis	Rubric Based	PO2	PSO1 PSO2	10
		Study of the Existing systems and feasibility of PBL proposal		PO4		10
Second review	Technical achievement	Review based comparison of existing system.	Rubric Based	PO3 PO12	PSO1 PSO2	15
		Identify and acquire information needed for design		PO5		15
Final review	Methodology and expected outcome of the proposed work	Originality of the project Idea	Rubric Based	PO3	PSO1 PSO2	10
		Methodology and design process		PO5		10
		Outcomes and deliverables		PO12		10
	PBL Report Evaluation	Quality of PBL Report		PO11		10
		Description of concepts And Knowledge of contemporary issues		PO12		10
TOTAL						100

LIST OF OPEN ELECTIVES

Open Elective II		
Course Code	Course	BOS
20NHOP701	Big Data Analytics using HP Vertica-1	CSE
20NHOP702	VM Ware Virtualization Essentials-1	ISE
20NHOP704	Big Data Analytics using HP Vertica-2	CSE
20NHOP705	VM Ware Virtualization Essentials-2	ISE
20NHOP707	SAP	MEE
20NHOP708	Schneider-Industrial Automation	EEE
20NHOP709	Cisco-Routing and Switching-1	ECE
20NHOP710	Data Analytics	CSE
20NHOP711	Machine learning	MEE
20NHOP712	CISCO-Routing and switching - 2	ECE
20NHOP713	IIOT Embedded System	MEE
20NHOP714	Block Chain	CSE
20NHOP715	Product Life cycle management	MEE
20NHOP717A	Network Security and Cryptography	ECE
20NHOP718A	Physical Design	ECE
20NHOP719A	AI Data Analysis with Python	AI&ML

RECOMMENDATIONS OF THE BOARD

The agenda was already circulated among the committee members and the following discussions were made based on the agenda.

Dr Lakshminarayana C, Professor in EEE, BMS College of Engineering, Bangalore and the VTU nominee attended the meeting along with Dr.K. Shanmukha Sundar Professor, Dayananda Sagar Academy of Technology and Management, Bengaluru, Mr. Rajashekhar S, General Manager, Open Systems International (An Emerson Company), Bengaluru, Dr. V Kamalakannan, Senior Design Engineer, Tessolve Semiconductor Pvt. Ltd., Bengaluru and meritorious alumni. The members appreciated the curriculum and syllabi.

Subject 1: Electromagnetic field theory

Dr Lakshminarayana C recommended that the course electromagnetic field theory has to be included in the NEP scheme. Since, the course is important for GATE exam preparation and for getting placed in industries and for higher education.

Subject 2: Suggestion to include fundamental and design thinking courses

Dr Lakshminarayana C suggested to include fundamental courses and design thinking courses for higher semesters. He also mentioned to reframe the mathematics syllabus based on the application of electrical engineering. The biology for engineers' course has to be added in the curriculum.

Subject 3: COs and POs framing and mapping

Dr Lakshminarayana C and Dr.K. Shanmukha Sundar given their suggestions for COs-POs mapping and COs framing procedure. They emphasized to highlight the RBT levels based COs. Dr.K. Shanmukha Sundar suggested to revisit the CO-PO mapping of all the courses and could be reviewed properly. He opined that the subject teachers have to give justification for their course CO- PO mapping and he mentioned that the RBT levels can be included in the syllabus. This observation is acknowledged and informed that it would be discussed in the appropriate forum.

Subject 4: Inclusion of E-books as Reference books

The VTU nominee suggested to include E-books, video links and you tube links related to particular course in reference section of syllabus.

Subject 5: Merging of DC and AC machines as a single course

Mr. Rajashekhar S opined that the DC and AC machines course can be made as a single course. It is informed to the course and module coordinators to find the possibilities of merging the courses and to incorporate in the syllabus.

Subject 6: Industry mentor for each course and audit certification course

Mr. Rajashekhar S suggested to include domain based courses to the students and based on their choice the specialized industry experts can serve as a mentor for each course. He also informed to collaborate with industry and to teach each course by faculty and an industry expert. It was informed to him that the EEE department is organising each semester industry experts' guest lecture and webinars for various courses. He mentioned to include audit certification courses for the missed courses for NEP scheme.

Subject 7: Lab courses

The alumni students Mr Naimish and Mr Bhavan informed to include electrical domain based laboratory courses in the curriculum and to update the new version of software tools.

VOTE OF THANKS BY THE CHAIRMAN-BoS

The Chairman thanked all the members for having participated in the meeting and contributed in framing the curriculum and syllabi for 2021-2025 batch.

