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# **Department of Electrical and Electronics Engineering**

## Report on ALUMNI TALK

### Introduction to VLSI and its various Industrial Domains



#### Department of Electrical and Electronics Engineering

Organizing

Alumni talk

On

# Introduction to VLSI and its various Industrial Domains

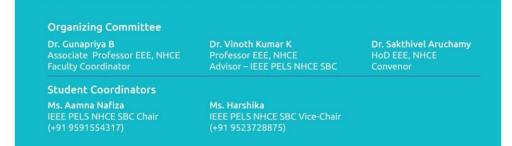


# Mr. Amogh Naragund

1NH17EE402 Physical Design Engineer in Mediatek Bengaluru, India.

📆 30 | November 2023 | 2:00 PM-4:00 PM

**B\_205** 



The Department of EEE organized an Alumni Talk titled "Introduction to VLSI and Its Various Industrial Domains" on 30th November 2023, from 2:00 pm to 4:00 pm, featuring Mr. Amogh Naragund, a physical design engineer at MediaTek. During the session, Mr. Naragund reflected on his academic experiences and provided an overview of VLSI (Very Large Scale Integration). He discussed the evolving semiconductor industry, sharing insights on the demands and expectations within the VLSI sector and its emerging market. He also guided students on how to enter the core VLSI domain and offered advice on enhancing their skills to succeed in this field.

The Department of Electrical and Electronics Engineering planned this very educational and engaging seminar for the students under the guidance of Dr Sakthivel Aruchamy, HOD of EEE and Dr GUNAPRIYA.



The program concluded with vote of thanks by Harshika student of EEE.