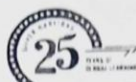


EEE



NEW HORIZON
COLLEGE OF ENGINEERING



NEW HORIZON
COLLEGE OF ENGINEERING

New Horizon Knowledge Park, Ring Road, Marathalli
Autonomous College Permanently Affiliated to VTU, Approved by AICTE & UGC
Accredited by NAAC with 'A' Grade, Accredited by NBA

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

11th BOARD OF STUDIES MEETING

DATE : 12.08.2025
VENUE : Schneider Electric Laboratory
(Room No: B-001)
TIME : 10 AM – 1 PM

Anandk
18/8/25

Head of the Department
Department of Electrical & Electronics Engineering
New Horizon College of Engineering
Ring Road, Marathalli, Bellandur Post,
Bangalore - 560103, Karnataka, India

Maytas
21/8

Principal

New Horizon College of Engineering
Ring Road, Bellandur Post,
Bangalore - 560 103.

Sl. No	PARTICULARS	Page No
1	Agenda for the meeting	3
2	List of Members	4
3	List of Members present	6
4	Agenda 1: Welcome address by Chairman of BOS and Introduction of members	9
5	Agenda 2: Approval of scheme and syllabus for the AY: 2025-2026, II-year (III and IV semester) EEE Program.	9
6	Agenda 3: Approval of scheme and syllabus for the AY: 2025-2026, IV-year (VII and VIII semester) EEE Program.	9
7	Agenda 4: Approval of Syllabus for Basic Electrical engineering and Elements of Electrical Engineering–Common first year course	10
8	Agenda 5: Revision of Vision, mission of the department	10
9	Recommendations of the Board	11
10	Vote of thanks by Chairman of BoS	12



DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

11th BOARD OF STUDIES MEETING

AGENDA FOR THE MEETING

Agenda 1: Welcome address by BoS Chairman.

Agenda 2: Approval of scheme and syllabus for the AY: 2025-2026, II-year (III and IV semester) EEE Program.

Agenda 3: Approval of scheme and syllabus for the AY: 2025-2026, IV-year (VII and VIII semester) EEE Program.

Agenda 4: Approval of Syllabus for Basic Electrical engineering and Elements of Electrical Engineering–Common first year course.

Agenda 5: Revision of Vision, mission of the department-If any

Agenda 6: Suggestions from BoS members.

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

LIST OF MEMBERS- BOARD OF STUDIES AY: 2025-2026

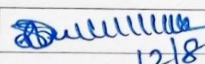
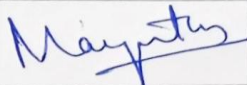
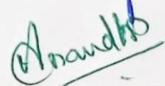
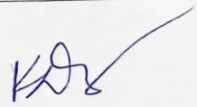
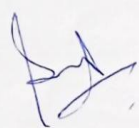
S. No	Category	Nomination of the committee	Name of the person	Designation & Affiliation
1	Head of the Department	Chairperson	Dr.Sujitha S	HoD & Professor, NHCE, Bangalore
2	Special Invitees (one academician from Institution of National Eminence, IIT, NIT, IIM,IISC)	1	Dr. Manjunatha	Principal, NHCE
		2	Dr. R. J Anandhi	Dean Academics, NHCE
		3	Dr. L Umanand	Professor, Center for Electronics and Design Technology (CEDT), Indian Institute of Science, Bangalore bangalore.lums@iisc.ac.in
3	Faculty member at different level with different specialization	Members		
		1	Dr.Vinoth Kumar K	Professor, NHCE
		2	Dr.Gunapriya B	Associate Professor, NHCE
		3	Dr Mohan Das R	Associate Professor, NHCE
		4	Dr.Karthika M	Associate Professor, NHCE
		5	Ms Sangeetha C N	Assistant Professor, NHCE
4	Subject expert from outside the college nominated by Academic Council	Members		
		1.	Dr. K. Deepa	Associate Professor, Electrical and Electronics Engineering, Amrita School of Engineering, Amrita Vishwa Vidyapeetham University, Amrita Nagar, Choodasandra, Junnasandra, Bengaluru, Karnataka 560035, k_deepa@blr.amrita.edu 9980005071
		2.	Dr P B Savitha	Associate Professor, Electrical and Electronics Engineering, Dayananda Sagar College of Engineering, Shavige Malleshwara HillsKumaraswamy Layout, Bengaluru, Karnataka-560078, savithapb-eee@dayanandasagar.edu 90087 07141

S. No	Category	Nomination of the committee	Name of the person	Designation & Affiliation
5	Experts from outside the college nominated by VTU	Member		
		1	Dr. Samanvita N	Professor, NITTE Meenakshi Institute of Technology, Bengaluru-560064 Samanvitha.n@nmit.ac.in 9731777517
6	Representative from Industry / Corporate sector / allied area related to placements, nominated by Academic Council	Members		
		1	Dr Praveen Kumar Reddy	DFT ENGINEER, Truechip solutions Pvt ltd, Ramsai Paradise, 610, opposite cafe coffee day, AECS Layout - C Block, AECS Layout, Marathahalli, Bengaluru- 560037 kpkreddy_99@yahoo.co.in 8124814373
		2	Mr Leela Prasad M R	Deputy Manager, Mahindra last mile mobility, Bengaluru-560099 Mrleelaprasad5075@gmail.com 7373435073
		3	Ms Mohana Venkatappa	Assistant HR Manager Department of HRD, NHCE 9606858059
7	Under Graduate meritorious alumni nominated by Principal	Members		
		1	Mr Amogh Narugund	Physical Design engineer 2, AMD EPIP Zone, Whitefield, Bengaluru amoghyn17@gmail.com 7406654730
		2	Mr Dayas Dixen	Electrical Designer, Schneider Electric, Rga Tech Park L&T, Dxc Technology, Tower 2, Carmelaram, Hadosiddapura, Doddakannelli, Bengaluru -560035 dayasdixen2020@gmail.com 8867415649
8	Co-opted members	Members		
		1	Mr.Vinod kumar S	Senior Assistant Professor, NHCE
		2	Mr. Sunil S K	Senior Assistant Professor, NHCE
		3	Ms Anitha A	Senior Assistant Professor, NHCE
		4	Ms Soumya K V	Assistant Professor, NHCE
		5	Ms Anitha Nair A S	Assistant Professor, NHCE
		6	Dr Sujoy Das	Senior Assistant Professor, NHCE

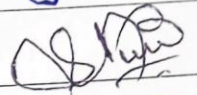
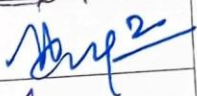


DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

LIST OF BOS MEMBERS PRESENT IN THE MEETING (AY: 2025-2026)-12.08.2025

S.NO	NAME	DESIGNATION & AFFILIATION	SIGNATURE
BoS Chairman			
1	Dr.Sujitha S	HoD & Professor, NHCE, Bangalore	 12/8/2025
Special Invitees			
2	Dr. Manjunatha	Principal, NHCE	
3	Dr. R. J Anandhi	Dean Academics, NHCE	
4	Dr. L Umanand	Professor, Center for Electronics and Design Technology (CEDT), Indian Institute of Science, Bangalore, lums@iisc.ac.in	ONLINE
Academic Expert			
5	Dr. K. Deepa	Associate Professor, Electrical and Electronics Engineering, Amrita School of Engineering, Amrita Vishwa Vidyapeetham University, Amrita Nagar, Choodasandra, Junnasandra, Bengaluru, Karnataka 560035, k_deepa@blr.amrita.edu 9980005071	
6	Dr P B Savitha	Associate Professor, Electrical and Electronics Engineering, Dayananda Sagar College of Engineering, Shavige Malleshwara HillsKumaraswamy Layout, Bengaluru, Karnataka-560078, savithapb-eee@dayanandasagar.edu 90087 07141	
VTU nominee			
7	Dr. Samanvita N	Professor, NITTE Meenakshi Institute of Technology, Bengaluru-560064 samanvita.n@nmit.ac.in 9731777517	

Industry Experts			
8	Dr Praveen Kumar Reddy	DFT ENGINEER, Truechip solutions Pvt Ltd, Ramsai Paradise, 610, opposite cafe coffee day, AECS Layout - C Block, AECS Layout, Marathahalli, Bengaluru, Karnataka 560037, kpkreddy_99@yahoo.co.in 8124814373	<i>Kpkreddy</i> <i>12/8/20</i>
9	Mr Leela Prasad M R	Deputy Manager, Mahindra last mile mobility, Bengaluru-560099 Karnataka Mrleelaprasad5075@gmail.com 7373435073	ONLINE
10	Ms Mohana Venkatappa	Assistant HR Manager Department of HRD, NHCE 9606858059	<i>Mohana V</i> <i>13/8/20</i>
Faculty member at different level with different specialization			
11	Dr.Vinoth Kumar K	Professor, NHCE	<i>Vinoth</i> <i>12/8/20</i>
12	Dr.Gunapriya B	Associate Professor, NHCE	<i>Gunapriya</i>
13	Dr. Mohan Das R	Associate Professor, NHCE	on duty
14	Dr.Karthika M	Associate Professor, NHCE	<i>Karthika</i>
15	Ms Sangeetha C N	Senior Assistant Professor, NHCE	<i>Sangeetha</i>
Meritorious alumni			
16	Mr Amogh Narugund	Physical Design engineer 2, AMD EPIP Zone, Whitefield, Bengaluru amoghyn17@gmail.com 7406654730	<i>Amogh</i>
17	Mr Dayas Dixen	Electrical Designer, Schneider Electric, Rga Tech Park L&T, Dxc Technology, Tower 2, Carmelaram, Hadosiddapura, Doddakannelli, Bengaluru, Karnataka 560035 dayasdixen2020@gmail.com 8867415649	<i>Dayas</i>
Co-opted faculty members			
18	Ms Anitha A	Senior Assistant Professor, NHCE	<i>Anitha</i>

19	Mr Sunil S K	Senior Assistant Professor, NHCE	
20	Mr Vinod Kumar S	Senior Assistant Professor, NHCE	
21	Ms Soumya K V	Assistant Professor, NHCE	
22	Ms Anitha Nair A S	Assistant Professor, NHCE	
23	Dr Sujoy Das	Senior Assistant Professor, NHCE	



NEW HORIZON COLLEGE OF ENGINEERING

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

WELCOME ADDRESS BY THE CHAIRMAN OF BOS AND INTRODUCTION OF MEMBERS

Minutes

Dr.Sujitha S, Chairman of BOS, welcomed the BoS members and introduced the significance of autonomy in the context of engineering education from industry perspective. The chairman briefed the gathering about the various regulations being followed in the department and emphasized the need for revision in curriculum and syllabi based on the inputs from various stake holders.

AGENDA -2

Approval of scheme and syllabus for the AY: 2025-2026, II-year (III and IV semester) EEE Program

Minutes

- *Scheme & Syllabus of II-year -2024-2028 Batch (III & IV semesters) has been reviewed.*
- *Suggestions from BoS members have been acknowledged and discussed in detail.*
- *Scheme & Syllabus of II Year 2024-2028 Batch (III & IV semesters) has been unanimously approved by all the members.*

AGENDA -3

Approval of scheme and syllabus for the AY: 2025-2026, IV-year (VII and VIII semester) EEE Program.

Minutes

- *Scheme & Syllabus of IV-year -2022-2026 Batch (VII & VIII semesters) has been reviewed.*
- *Suggestions from BoS members have been acknowledged and discussed in detail.*
- *Scheme & Syllabus of IV Year 2022-2026 Batch (VII & VIII semesters) has been unanimously approved by all the members.*



AGENDA -4

Approval of Syllabus for Basic Electrical engineering and Elements of Electrical Engineering –Common first year course

- *Syllabus of 1 year course Basic Electrical Engineering and Elements of Electrical Engineering of 2025-2029 Batch (I & II semesters) has been reviewed.*
- *Suggestions from BoS members have been acknowledged and discussed in detail.*
- *Syllabus of 1 year course Basic Electrical Engineering and Elements of Electrical Engineering of 2025-2029 Batch (I & II semesters) has been unanimously approved by all the members.*

AGENDA -6

Revision of Vision, mission of the department-If Any

- *All the BoS members are agreed to retain the Vision and Mission of the department and suggested to make modifications in the Program Specific Outcomes.*



Department of Electrical and Electronics Engineering
BoS meeting-AY: 2025-2026

RECOMMENDATIONS OF THE BOARD

The agenda and scheme and syllabus were already circulated among the committee members and the following recommendations were received by external BoS members.

I.Suggestions from Dr Samanvita N, VTU nominee, Professor in EEE, NITTE Meenakshi Institute of Technology, Bengaluru

1. Recommended that the courses on sustainability and certification on those courses has to be included to give more weightage for the global requirements.
2. FPGA with SiC experiments can be included in the syllabus.

II.Suggestions from Dr Deepa, Associate Professor, EEE, Amrita School of Engineering, Bengaluru.

1. In the measurements and instrumentation course, the Smart and Digital Metering Technologies such as Smart Energy Meters (IoT-enabled), data logging, remote monitoring can be included. Also, standards & communication protocols used in modern metering (e.g., Modbus, DLMS) and Digital Oscilloscopes (DSO) features and measurement techniques can be added.
2. In the Analog Electronics course, the Zener diode regulations, distortion in power amplifiers and methods of reduction, precision rectifier circuits and PWM generation using 555 for motor control can be included.

Action Taken: As per the discussion with course coordinator, the above topics are included in the syllabus.

III.Suggestions from Dr. Savitha P B,Associate Professor, Dayanand Sagar College of Engineering, Bengaluru

1. Basic sensor concept may be introduced, and various sensors used in the measurement of electrical quantities can be included in measurements.
2. The cryptography concepts can be included in module 1 for the course 24EEE541.
3. Wide band gap power electronics devices characteristics may be introduced, like SiC, GaN, in the first module.

Action Taken: These revisions will be carried out for the next academic year III year courses as there is no revision for that batch.

3. Module 1 Basics of solar energy like concept of Azimuth angle, etc, and solar cell equivalent circuit (single diode, double diode) and latest solar cell technology (Bifacial type) can be included.

Action Taken: As per the discussion with course coordinator, the above topics are included in the syllabus.

IV.Suggestions from Dr Praveen Kumar Reddy (Industry Expert), DFT ENGINEER, Truechip solutions Pvt ltd,Bengaluru

For the course CMOS VLSI design

1. Chapter 3 & 5 can be merged
2. Chapter 5 can be introduced to Manufacturing Defects in VLSI & Design for Test Methodologies
3. Module-1 Introduction to semiconductor testing. Importance of testing as a requisite for achieving manufacturing quality of semiconductor devices and then identifies difficulties in VLSI testing, Defects and their modeling as faults at gate level and transistor level. Various types of faults. Functional vs. Structural approach to testing, Complexity of the testing problem, Controllability and Observability, Generating test for a single stuck at fault in combinational logic, D-algorithm
4. Structural approach to testing - Scan Test

Action Taken: These revisions will be carried out for the next academic year III year courses as there is no revision for that batch.

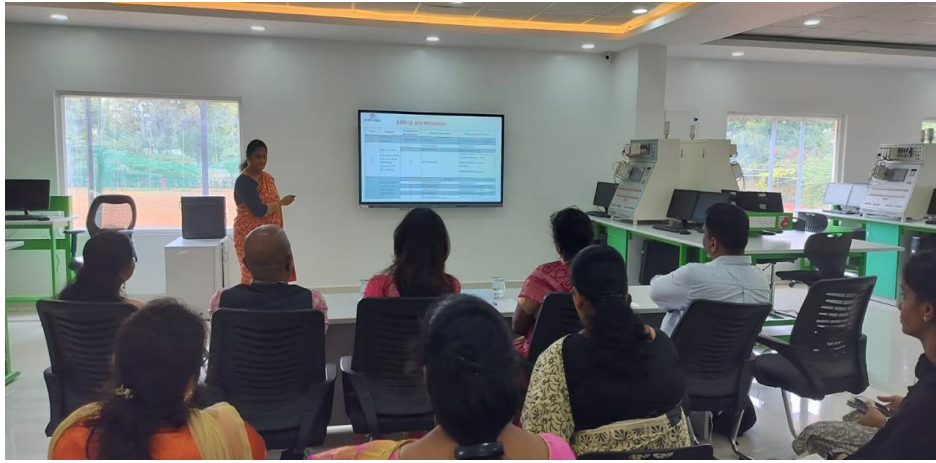
V.Revision of Vision, Mission, PEOS and PSOs:

All the BoS members are agreed to retain the Vision and Mission of the department and suggested to make modifications in the Program Specific Outcomes (PSOs).

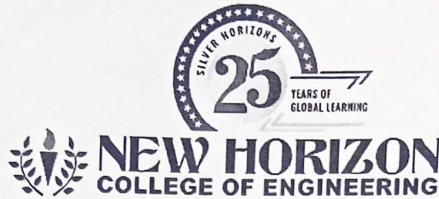
VOTE OF THANKS BY THE CHAIRMAN-BoS

The Chairman thanked all the members for having participated in the meeting and contributed in framing the curriculum and syllabus for 2024-2028 batch, and 2022-2026 batch.

Glimpses of BoS meeting for the AY: 2025-2026 –Dt: 12.08.2025



ANNEXURE-I



DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

BOS MEETING-AY: 2025-2026

12.08.2025

Existing PSOs of Department of Electrical and Electronics Engineering

PSO 1: Graduates will be able to solve real life problems of Power system and Power Electronics using MiPower, PSPICE and MATLAB software tools and hardware.

PSO 2: Graduates will be able to develop and support systems based on renewable and sustainable Energy sources.

Recommendations of the Board

As per the recommendations of all the BoS members and the stake holders the following modifications has been carried out in the Program Specific Outcomes (PSOs).

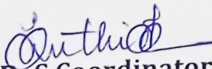
Recommended Programme Specific Outcomes (PSOs) for approval

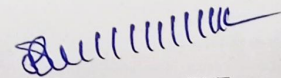
PSO1

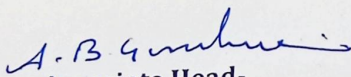
Apply knowledge of electrical machines, circuit analysis, electronics, power systems, control, instrumentation, power electronics, signal processing, and embedded systems to analyse, design, and implement solutions for real-world applications using modern hardware and software tools.

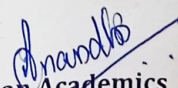
PSO2

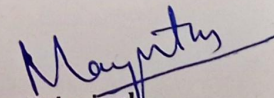
Design and develop innovative solutions in renewable energy, electric vehicles, smart grids, and automation with focus on environmental sustainability and societal needs.


BoS Coordinator


BoS Chairman-EEE


Associate Head-
Quality Assurance


Dean Academics
Dr P. Anandhi
Professor and Dean-Academics
New Horizon College of Engineering
Ring Road Bellandur Post
Bengaluru - 560 103


Principal
Principal
New Horizon College of Engineering
Ring Road, Bellandur Post, Near Marathalli
Bangalore - 560103