



Department of Electrical and Electronics Engineering

Expert Talk

Fundamentals of VLSI Design

Mr. Rameez Khan
Post Silicon Validation Engineer
Synopsys, Bangalore



📅 14th October 2025

🕒 9:30 AM - 11:30 AM

📍 Tejas Seminar Hall

🌸 5th Semester E.E.E Students

Coordinators

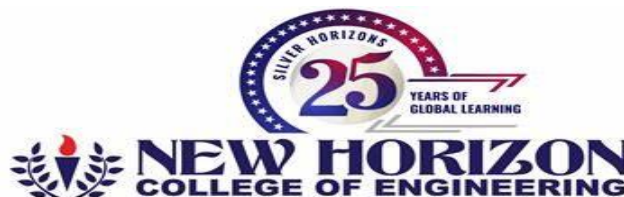
Prof. Sangeetha C N
Assistant Professor

Prof. Soumya K V
Assistant Professor

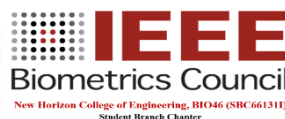
Convenor

Dr. S. Sujitha
HoD-EEE

Dr. Manjunatha
Principal



Department of Electrical and Electronics Engineering



Event Report

Title	Expert talk on “Fundamentals of VLSI Design”	
Department	Electrical and Electronics Engineering	
Date	From: 14.10.2025	To: 14.10.2025
Time	From: 09:30 AM	To: 11:30 AM
Brief Description	The Department of Electrical and Electronics Engineering, in association with IEEE NHCE Student Branch, IEEE IES, IEEE IES Bangalore Chapter, IEEE Biometrics Council and IEEE CSC , organized an expert talk on “Fundamentals of VLSI Design” on 14th October 2025. The session was conducted from 9:30 AM to 11:30 AM at Tejas Seminar Hall and was attended by 5th semester EEE students. The session was delivered by Mr. Rameez Khan, Post Silicon Validation Engineer at Synopsys, Bangalore. He began the session with an overview of the Silicon Development Life Cycle, illustrating the journey of semiconductor design from concept to market release. Using a creative analogy, he compared the process of chip development to the stages of movie production — from scripting (design) and shooting (verification) to post-production (validation) and release (fabrication). Mr. Khan explained the Pre-Silicon Phase, emphasizing RTL design, simulation, and verification using Verilog, with a practical example of designing an I²C communication module. He then elaborated on Post-Silicon Validation , covering device wake-up, manual debugging, and PVT (Process, Voltage, Temperature) characterization. The importance of automation in testing and data analysis for compliance was also discussed. Students gained valuable insights into the workflow of industry-standard chip manufacturing and testing environments. The session concluded with an interactive Q&A, where students actively participated, clarifying their doubts about VLSI career paths and technologies. The event received excellent feedback from attendees, who appreciated the clarity and industrial relevance of the talk.	

Images

