



Expert Lecture

Smart Prototyping for IoT and Embedded Systems Using Z Boards

 15 April 2026

 02:00 PM - 04:00 PM

 4th Semester Students

 Library Video Conference Room

Faculty Coordinator

Ms. Surat Pyari Atti
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Convenor

Dr. S Sujitha
HoD - EEE

Dr. Revathi V
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Principal

Organised by

Department Of Electrical and Electronics Engineering



Dr. M M Sravani
Staff Engineer,
Synopsys India Pvt Ltd,
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Department of Electrical and Electronics Engineering



EVENT REPORT

Title	Expert talk on Smart Prototyping for IoT and Embedded Systems Using Z Boards	
Department	Electrical and Electronics Engineering	
Date	From: 15.04.2026	To: 15.04.2026
Time	From: 2.00 PM	To: 4.00 PM
Brief Description	<p>The Department of Electrical and Electronics Engineering organized an Expert Talk on “Smart Prototyping for IoT and Embedded Systems Using Z Boards” on 15th April 2026 from 2:00 PM to 4:00 PM. The event was conducted in association with the IEEE Dielectrics & Electrical Insulation Society.</p> <p>The session was delivered by Dr. M M Sravani, Staff Engineer of Synopsys India Pvt. Ltd, aiming to provide industry exposure and practical insights into modern embedded and VLSI technologies.</p> <p>The expert introduced the concept of smart prototyping using Zynq-based boards, emphasizing the integration of Processing System (PS) and Programmable Logic (PL). The session covered various implementation platforms including FPGA, PYNQ Boards, and ZED Boards, highlighting their architecture, features, and real-time applications in IoT and embedded systems.</p> <p>A detailed overview of FPGA technology was presented, explaining its advantages in parallel processing and flexibility compared to conventional microcontrollers. The expert also demonstrated the use of PYNQ boards, showcasing how Python can be effectively used for hardware programming, making development faster and more accessible.</p> <p>Further, the session provided insights into the VLSI design flow, including design entry, synthesis, simulation, and physical design. Key stages of physical design, such as floor planning, placement, routing, and timing analysis, were clearly explained with industry relevance.</p> <p>The expert also discussed various career opportunities in domains like Embedded Systems, FPGA Design, VLSI Front-end, and Physical Design. Guidance was given on required skills, tools, and industry expectations, helping students understand potential career paths.</p> <p>The session was interactive, with students actively engaging in discussions and clarifying their queries related to technical concepts and career opportunities.</p>	

Overall, the lecture was highly informative and beneficial, enhancing students' understanding of advanced prototyping platforms and providing valuable direction for careers in embedded systems and VLSI domains. A total of 60 students attended the expert talk, and the session received very positive feedback for its informative and inspiring content.

Images





A handwritten signature in blue ink, appearing to be 'D. M.' with a flourish.

Event Co Ordinator

A handwritten signature in black ink, appearing to be 'B. S.' with a flourish.

HoD-EEE