



Autonomous College Permanently Affiliated to VTU, Approved by AICTE & UGC
Accredited by NAAC with 'A' Grade, Accredited by NBA

Department of Electrical and Electronics Engineering
Report on ALUMNI TALK

Alumni Talk Report

Department of Electrical and Electronics Engineering

New Horizon College of Engineering, Bengaluru

In association with **IEEE NHCE Student Branch** and **IEEE Transportation Electrification Council (TEC)**

Event Title

“Chips, Circuits and Careers: Exploring the VLSI Industry”
(Final Year Readiness Talk Series)

Date and Venue

30th March 2026

03:00 PM – 05:00 PM

Tejas Seminar Hall, New Horizon College of Engineering

Resource Person

Mr. Srinivas Abhinav Gandla

VLSI Analyst, Capgemini Engineering

Alumnus – Batch **2021–2025**

Organized By

Department of Electrical and Electronics Engineering in association with the **Alumni Association, IEEE NHCE Student Branch, and IEEE Transportation Electrification Council (TEC).**

Event Overview

The Department of Electrical and Electronics Engineering organized an insightful **Alumni Talk** titled *“Chips, Circuits and Careers: Exploring the VLSI Industry”* on **30th March 2026** at **Tejas Seminar Hall** as part of the **Final Year Readiness Talk Series**.

The session was delivered by **Mr. Srinivas Abhinav Gandla**, an alumnus of the EEE Department and currently working as a **VLSI Analyst at Capgemini Engineering**. The objective of the talk was to introduce students to the rapidly growing **VLSI (Very Large-Scale Integration) industry**, provide guidance on career opportunities in semiconductor design, and help final-year students prepare for roles in the semiconductor and chip design ecosystem.

The event was organized under the guidance of **Dr. S Sujitha (HoD – EEE)** and coordinated by **Dr. B Gunapriya (Alumni Coordinator)**. The program was supported by the institutional leadership including **Dr. Revathi V (Dean – R&D)**, **Dr. R J Anandhi (Dean – Academics)**, and **Dr. Manjunatha (Principal)**.



The poster is for an "Alumni Talk" titled "Chips, Circuits and Careers: Exploring the VLSI Industry". It is part of the "Final Year Readiness Talk Series". The event is scheduled for 30th March 2026, from 03:00 PM to 05:00 PM, at Tejas Seminar Hall. The speaker is Mr. Srinivas Abhinav Gandla, a VLSI Analyst at Capgemini Engineering, Batch: 2021-2025. The event is organized by the Department of Electrical and Electronics Engineering | Alumni Association. Logos for IEEE NHCE Student Branch, New Horizon College of Engineering (25th Anniversary), and IEEE TEC are displayed at the top.

Alumni Coordinator: **Dr. B Gunapriya**
Convener: **Dr. S Sujitha** (HoD - EEE)
Dr. Revathi V (Dean - R & D)
Dr. R J Anandhi (Dean - Academics)
Dr. Manjunatha (Principal)

Organised by
Department of Electrical and Electronics Engineering | Alumni Association





Key Highlights of the Session

1. Introduction to the VLSI Industry

Mr. Srinivas provided an overview of the **global semiconductor industry**, emphasizing the importance of VLSI technology in modern electronics. He explained how integrated circuits form the backbone of technologies such as:

- Smartphones and consumer electronics
- Artificial Intelligence hardware
- Automotive electronics
- Embedded systems
- Communication and networking systems

He also discussed the increasing demand for semiconductor professionals due to global technological advancements.

2. VLSI Design Flow and Industry Roles

The speaker elaborated on the **VLSI design flow**, giving students a structured understanding of the semiconductor development process, including:

- RTL Design
- Functional Verification
- Physical Design
- Static Timing Analysis
- Design for Testability (DFT)

He also explained the different roles available in the VLSI industry such as:

- Design Engineer
- Verification Engineer
- Physical Design Engineer
- FPGA Engineer
- Analog/Mixed Signal Designer

This helped students understand the variety of career paths available within the semiconductor ecosystem.

3. Skills Required for VLSI Careers

Mr. Srinivas emphasized that strong fundamentals are crucial for entering the VLSI field. He advised students to build expertise in core subjects such as:

- Digital Electronics
- CMOS VLSI Design
- Semiconductor Physics
- Computer Architecture

He also highlighted the importance of learning industry tools and programming skills such as:

- Verilog / SystemVerilog
- VHDL
- Python / TCL scripting
- EDA tools used for chip design

Students were encouraged to undertake **mini projects and internships** to strengthen their practical knowledge.

4. Career Preparation Strategies

The session also focused on practical steps for students to prepare for semiconductor industry roles. The speaker shared valuable insights on:

- Building a strong technical resume
- Preparing for technical interviews
- Participating in internships and industry training programs
- Developing project portfolios related to digital design and FPGA implementation

He also explained how students can leverage online learning platforms and certification programs to enhance their technical skills.

5. Industry Trends and Future Opportunities

Mr. Srinivas highlighted emerging opportunities in areas such as:

- AI hardware accelerators
- Automotive electronics and EV technology
- IoT chip design
- Edge computing hardware
- Semiconductor manufacturing initiatives in India

He emphasized that the semiconductor sector is expected to grow significantly, creating vast opportunities for engineering graduates.

Student Interaction

The talk concluded with a lively **interactive question-and-answer session**, where students actively engaged with the speaker. Questions were asked regarding:

- Entry pathways into the VLSI industry
- Differences between VLSI and embedded system careers
- Important tools and certifications to pursue
- Internship opportunities in semiconductor companies

Mr. Srinivas addressed these queries with practical advice and encouraged students to remain curious and continuously upgrade their technical skills.

Outcomes of the Event

The alumni talk achieved the following outcomes:

1. Students gained a clear understanding of the **VLSI design ecosystem and semiconductor industry landscape**.
2. Participants learned about **different career roles and skill requirements** in the chip design industry.
3. Final-year students received **valuable guidance on preparing for technical interviews and industry expectations**.
4. The session strengthened **industry-academia interaction through alumni engagement**.
5. Students were motivated to explore **research, internships, and projects in VLSI and semiconductor technologies**.

Conclusion

The alumni talk by **Mr. Srinivas Abhinav Gandla** was highly informative and beneficial for the students of the Electrical and Electronics Engineering Department. His insights into the **VLSI industry, semiconductor technologies, and career preparation strategies** provided students with a clear roadmap for pursuing opportunities in this rapidly growing field. The Department of EEE expressed sincere gratitude to the speaker for sharing his expertise and inspiring students to build strong technical foundations for successful careers in the semiconductor industry. The session concluded with a vote of thanks, marking the event as a valuable learning experience for all participants.

